

WIZ750SR-100 Datasheet

Supported Languages

- English only



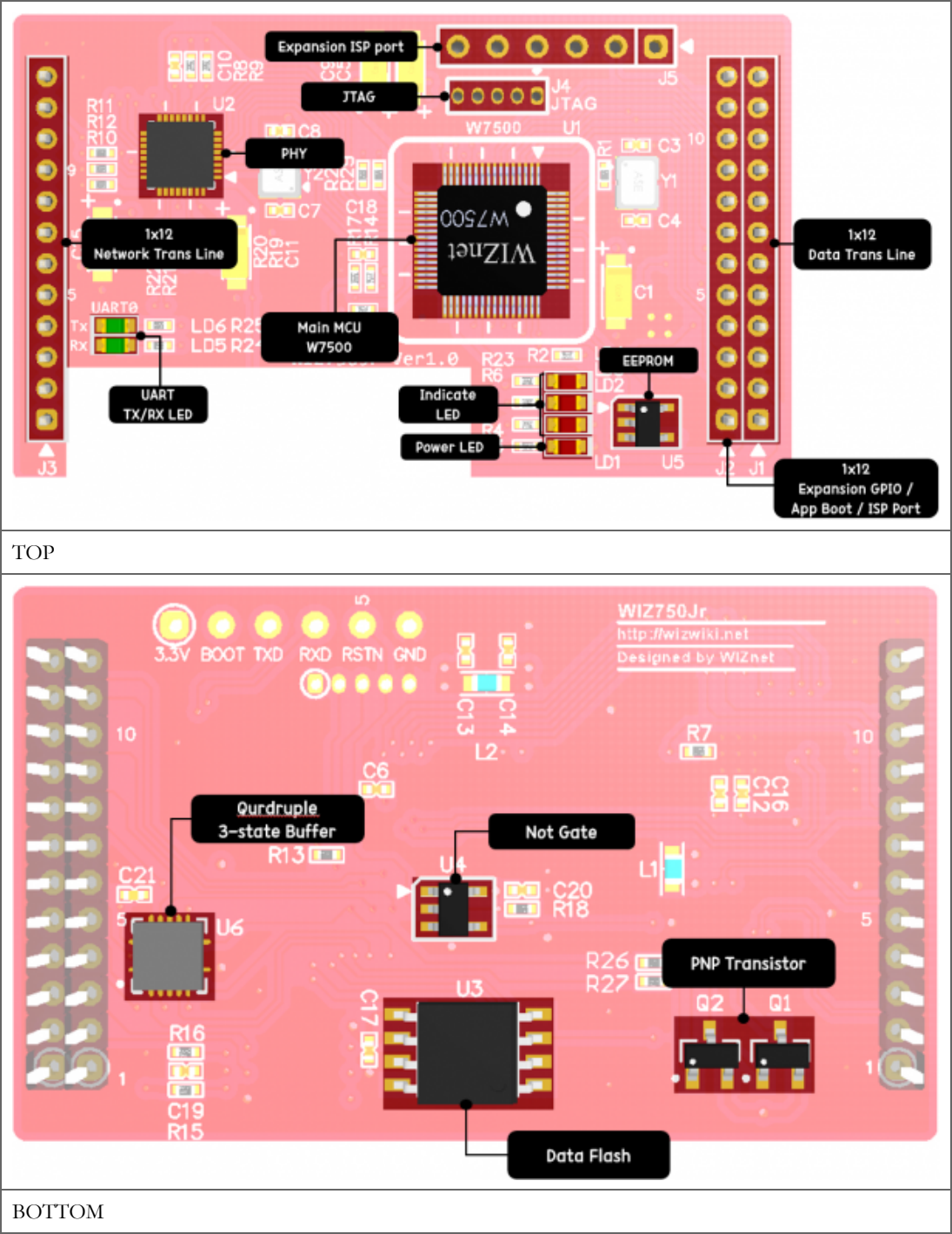
Hardware Specification

Product Spec Table

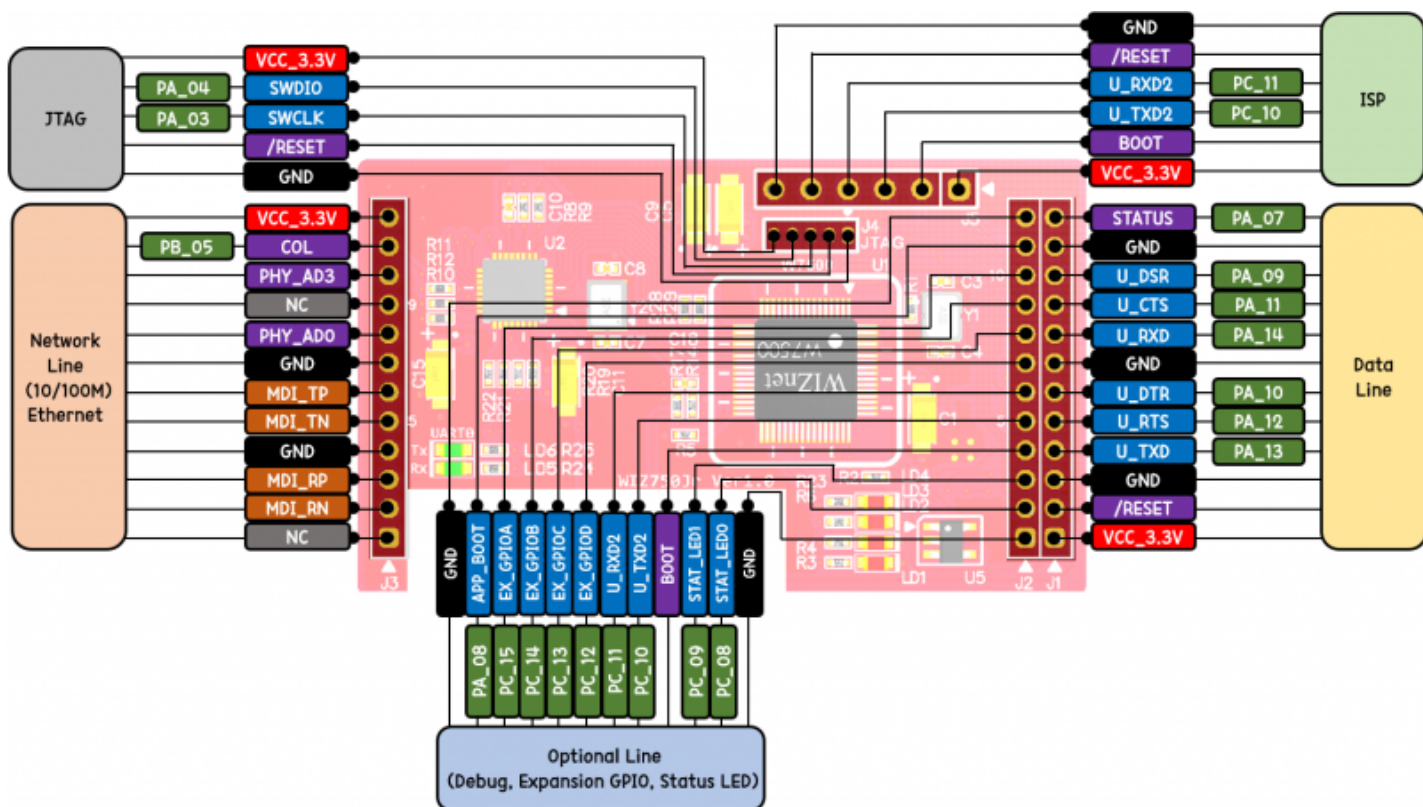
Category		Description
MCU	ARM Cortex-M0 Core	W7500 48Mhz maximum frequency Internal 8Mhz RC Oscillator Flash: 128KB Large flexible-size SRAM buffer for various User Application - Min 16KB available if full 32KB socket buffer used - Max 48KB available if no socket buffer used ROM for boot code: 6 KB
	Hardwired TCP/IP Core	8 independent Sockets SRAM for socket: 32KB MII (Medium-Independent Interface) TCP/IP Protocols: TCP, UDP, ICMP, IPv4, ARP, IGMP, PPPoE
PHY	Transceiver	IP101GRI Single 10/100M Ethernet Transceiver
Serial	Interface	WIZ750SR-100: TTL Version WIZ750SR-EVB: RS-232 Version
	Signal	TXD, RXD, RTS, CTS, GND
	Parameters	Parity: None, Odd, Even Data bits: 7, 8 bit Flow control: None, RTS / CTS, XON / XOFF
	Speed	Up to 230Kbps
Dimension		50mm x 30mm (PCB board size) 50mm x 30mm x 12mm (Include part size)

Category	Description
Connector type	2.00mm Pitch 1×12 Pin-header(Data Line) 2.00mm Pitch 1×12 Pin-header(Network Line) 2.00mm Pitch 1×12 Pin-header(Expansion GPIO & app_boot & ISP) 1.27mm Pitch 1×5 Pin-header (JTAG)
Input Voltage	DC 3.3V
Temperature	-40℃ ~ 85℃ (Operation), -40℃ ~ 85℃ (Storage range)

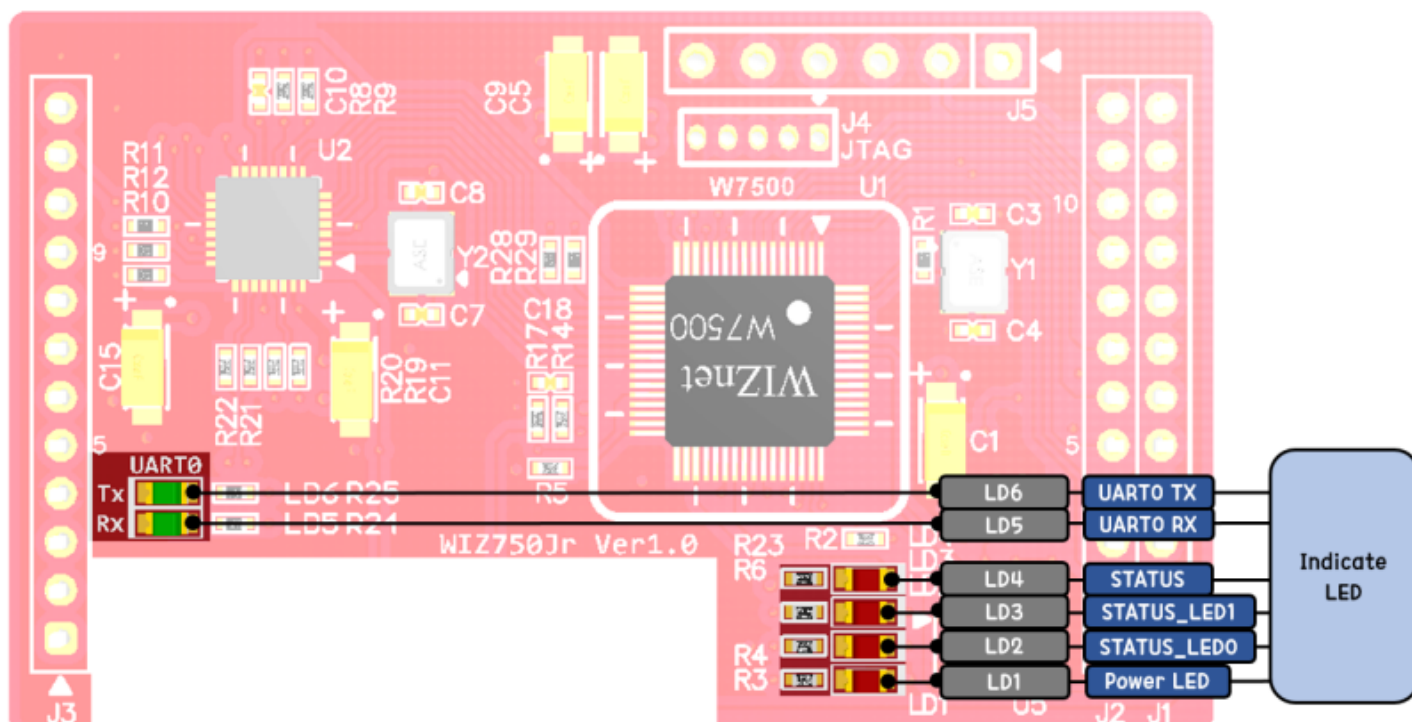
WIZ750SR-100 Callout



WIZ750SR-100 Pinout

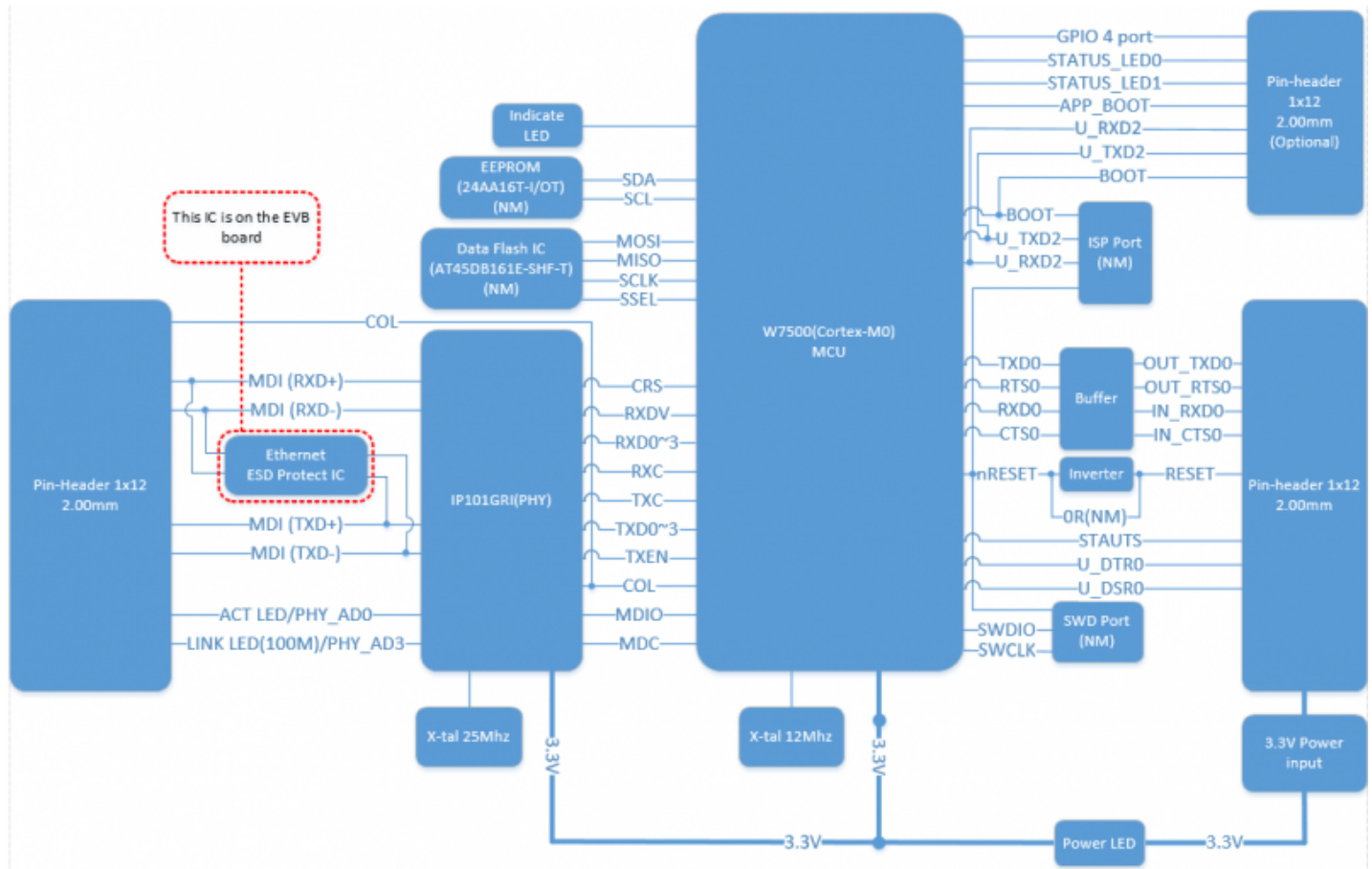


WIZ750SR-100 indicate



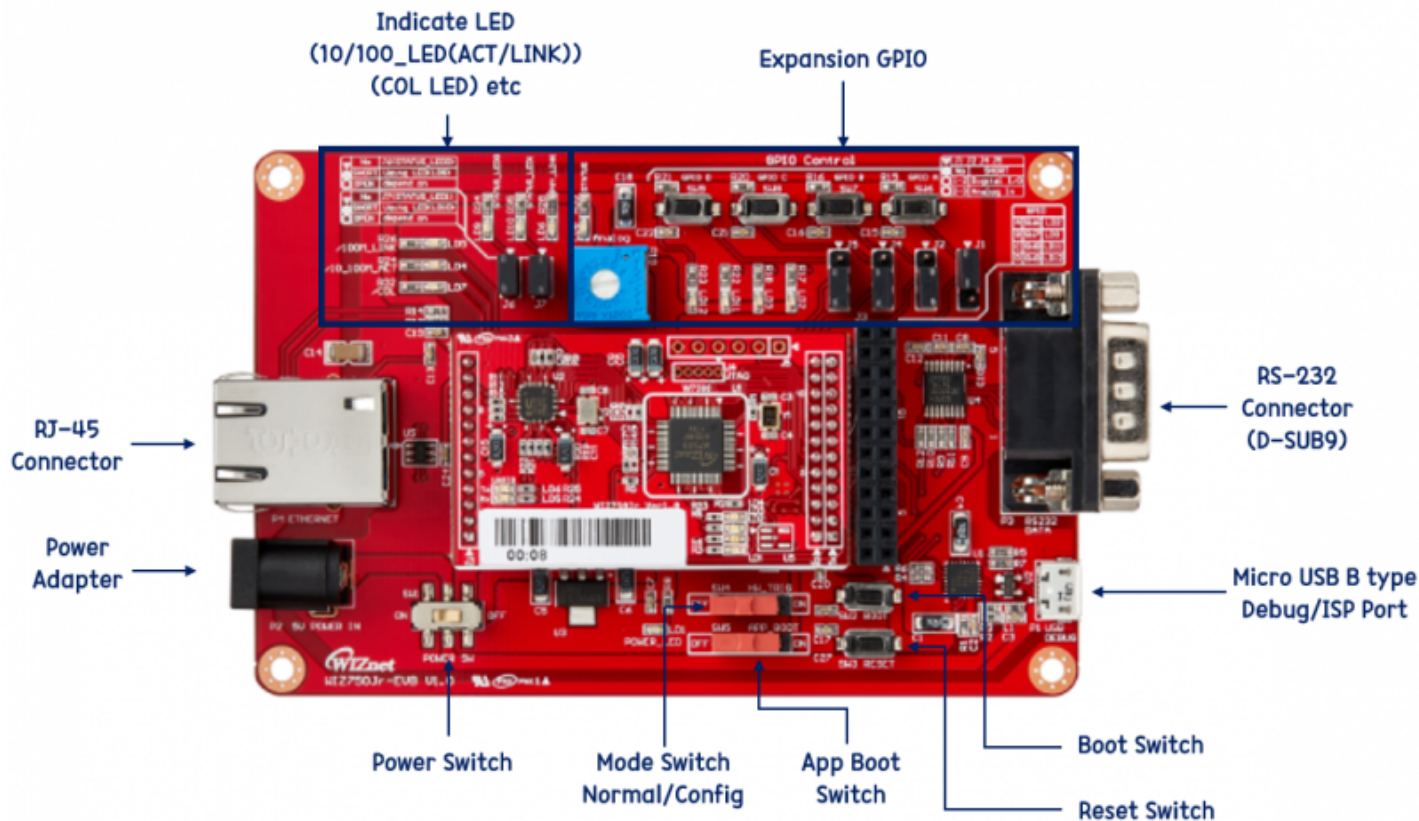
Pin Number	Pin Name	Signal	Description
1	LD1	Power LED	-
2	LD2	Status_LED0	PHY LINK check or initialize done
3	LD3	Status_LED1	TCP Connectoin
4	LD4	STATUS	Not function
5	LD5	UART0 RX	UART receiver indicate
6	LD6	UART0 TX	UART Transceiver indicate

WIZ750SR-100 Block Diagram



WIZ750SR-100-EVB

- WIZ750SR-100 Developer Board.
- USB to UART chip, CP2104.
- RESET Tact SW.
- BOOT0 Slide SW.
- H/W Trig Slide SW.
- Expansion GPIO TEST (Digital / Analog)
- Micro USB.
- WIZ750SR-100-EVB (RS-232)
 - RS-232C Transceiver, D-SUB9-MALE.



Schematic & Artwork

WIZ750SR-100

H/W version	Type	Filetype	Download Link	Remarks
1.0	TTL	Altium	Download	-
		PDF	Download	-

WIZ750SR-EVB

H/W version	Type	Filetype	Download Link	Remarks
1.0	RS232	Altium	Download	-
		PDF	Download	-


Part list

WIZ750SR-100

H/W version	Type	Filetype	Download Link	Remarks
1.0	TTL	Excel	Download	
		PDF	Download	

WIZ750SR-100-EVB

H/W version	Type	Filetype	Download Link	Remarks
1.0	TTL	Excel	Download	

H/W version	Type	Filetype	Download Link	Remarks
		PDF	 Download	

Electrical Characteristics

Operating Conditions

Symbol	Parameter	Pins	Min	Typ	Max	Unit
V _{cc}	Operating Voltage	3.3V	2.7	3.3	3.6	V
V _{ss}	Ground	ALL		0	50	mV
f _{CLK}	Internal CPU clock frequency	ALL	0	-	48	MHz
T _{stg}	Storage Temperature (max)	ALL	-40		85	°C
T _A	Ambient operating temperature	ALL	-40	85		°C
V _{IO}	I/O Signal voltage (Tolerance)	ALL	V _{ss} -0.3	3.3	5	V
V _{IH}	Input high voltage	ALL	2.145			V
V _{IL}	Input low voltage	ALL			1.155	V
V _{OH}	Output high voltage (High driving strength Current load = 6mA) (Low driving strength Current load = 3mA)	ALL	2.5			V
V _{OL}	Output high voltage (High driving strength Current load = 6mA) (Low driving strength Current load = 3mA)	ALL			0.5	V

Flash Memory

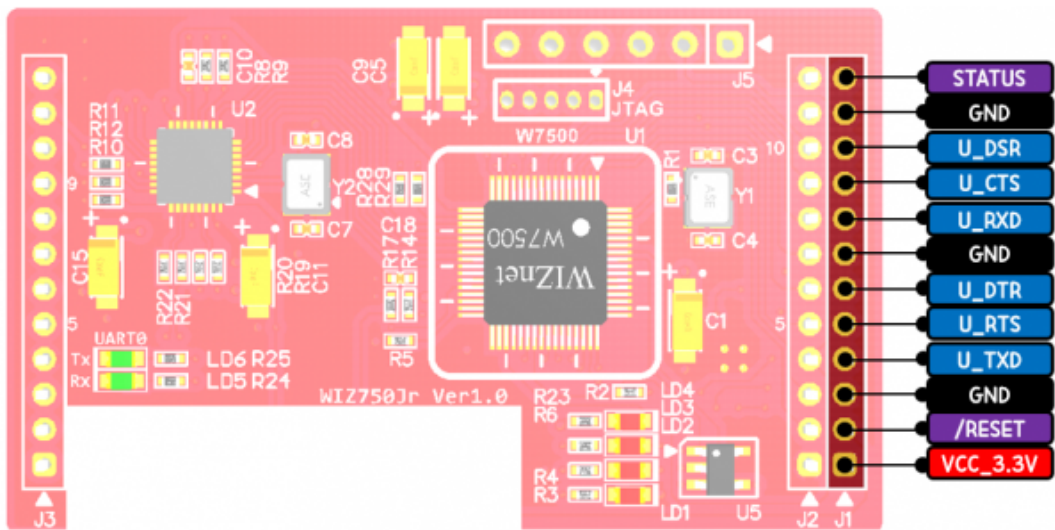
Symbol	Parameter	Min	Unit
NEND	Sector Endurance	10,000	Cycles
TDR	Data Retention	10	Years

EEPROM

Symbol	Parameter	Min	Unit
NEND	Sector Endurance	1M	Cycles
TDR	Data Retention	200	Years

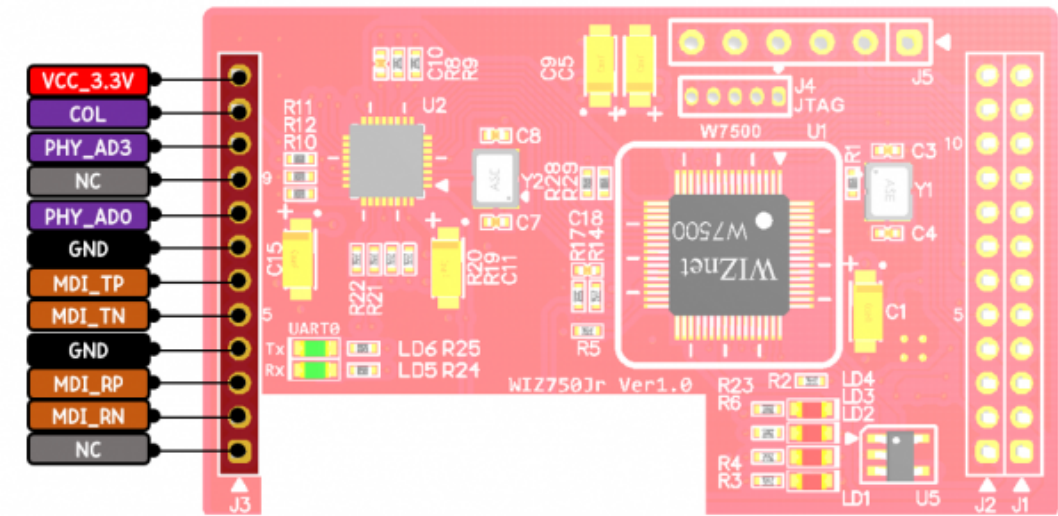
Connector Specification

1x12 Data Pin Connector (J1)



Pin Number	Signal	Description
1	VCC	System Power input (3.3V)
2	nRESET	System Reset signal (Active Low)
3	GND	System Ground
4	U_TXD0	Transmit Data (TTL : 3.3V)
5	U_RTS0	Request To Send (TTL : 3.3V)
6	U_DTR0 PHY LINK	Data Terminal Ready (TTL : 3.3V) PHY Link status check signal
7	GND	System Ground
8	U_RXD0	Receive Data (TTL : 3.3V)
9	U_CTS0	Clear To Send (TTL : 3.3V)
10	U_DSR0 TCP CON HW_TRIG	Data Set Ready (TTL : 3.3V) TCP Connection check signal Hardware Trigger signal (Active Low)
11	GND	System Ground
12	STATUS	User Depend on indicate LED pin

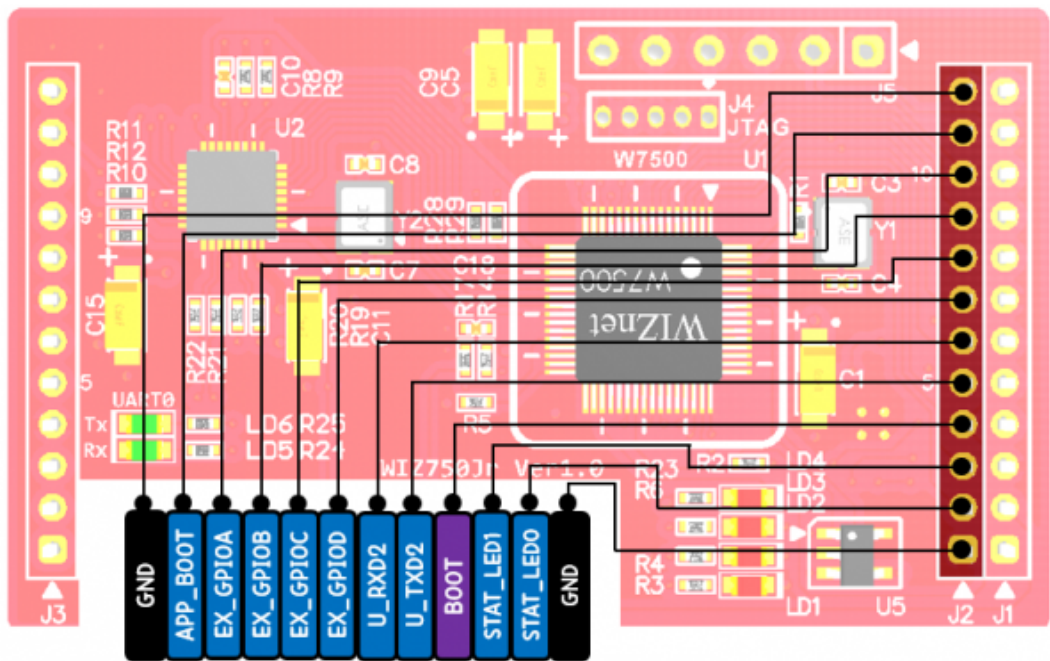
1x12 Network Line (J3)



Pin Number	Signal	Description
1	NC	Not Connect

Pin Number	Signal	Description
2	MDI_RN	10/100M MDIO Receiver Differential Signal (N signal)
3	MDI_RP	10/100M MDIO Receiver Differential Signal (P signal)
4	GND	System Ground
5	MDI_TN	10/100M MDIO Transceiver Differential Signal (N signal)
6	MDI_TP	10/100M MDIO Transceiver Differential Signal (P signal)
7	GND	System Ground
8	PHY_AD0	Link LED (When the PHY is physically connected, the LINK LED lights up.)
9	NC	Not Connect
10	PHY_AD3	ACT LED (ACT LED blinks when data is transmitted/received between PHYs.)
11	COL	System Ground
12	VCC	System Power input (3.3V)

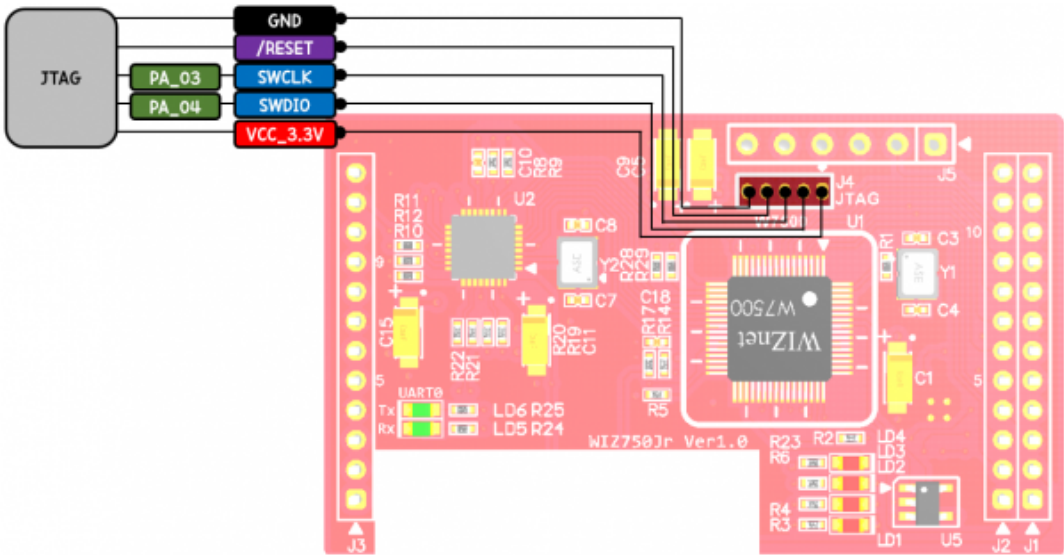
1x12 Expansion GPIO & APP BOOT & Debug(ISP) Port (J2)



Pin Number	Signal	Description
1	GND	System Ground
2	Status LED0	User's depend on pin(LED)
3	Status LED1	User's depend on pin(LED)
4	BOOT	BOOT SW
5	U_TXD2	Simple UART2(Debug port) ISP mode firmware downloader port
6	U_RXD2	Simple UART2(Debug port) ISP mode firmware downloader port
7	Expansion GPIOD	Expansion User's depend on GPIO port
8	Expansion GPIOC	
9	Expansion GPIOB	
10	Expansion GPIOA	

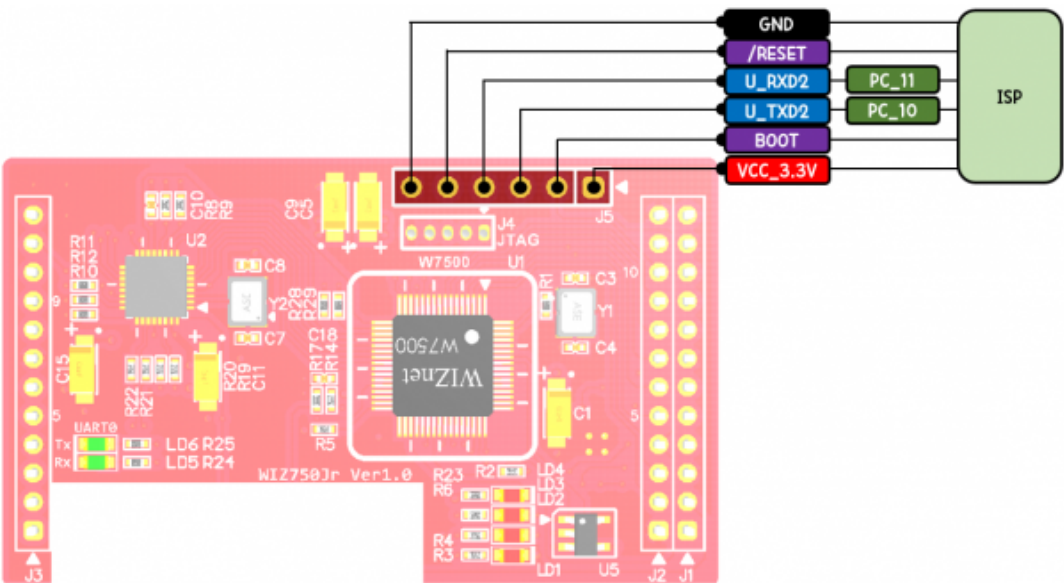
Pin Number	Signal	Description
11	APP BOOT	Application Jump at BOOT mode
12	GND	System Ground

1x5 JTAG



Pin Number	Signal	Description
1	VCC	System Power input (3.3V)
2	SWDIO	SWD(JTAG) Data I/O pin
3	SWCLK	SWD(JTAG) Clock pin
4	nRESET	System Reset signal (Active Low)
5	GND	System Ground

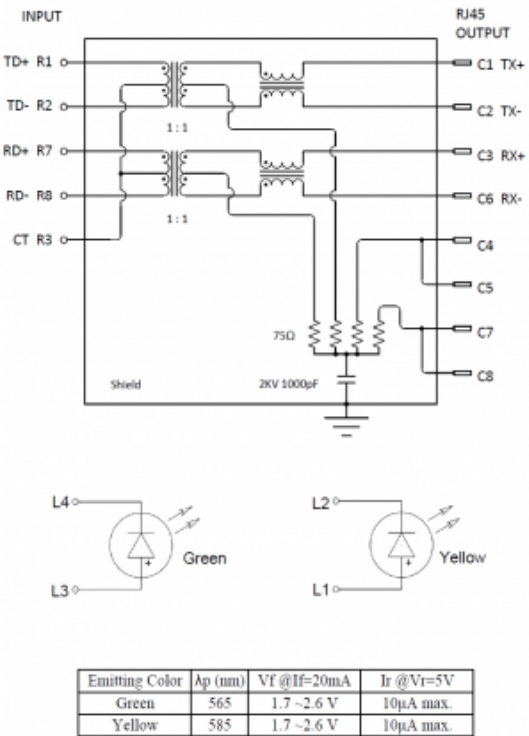
1x6 ISP



Pin Number	Signal	Description
1	VCC	System Power input (3.3V)
2	BOOT	BOOT SW

Pin Number	Signal	Description
3	U_TXD2	Simple UART2(Debug port) ISP mode firmware downloader port
4	U_RXD2	Simple UART2(Debug port) ISP mode firmware downloader port
5	nRESET	System Reset signal (Active Low)
6	GND	System Ground

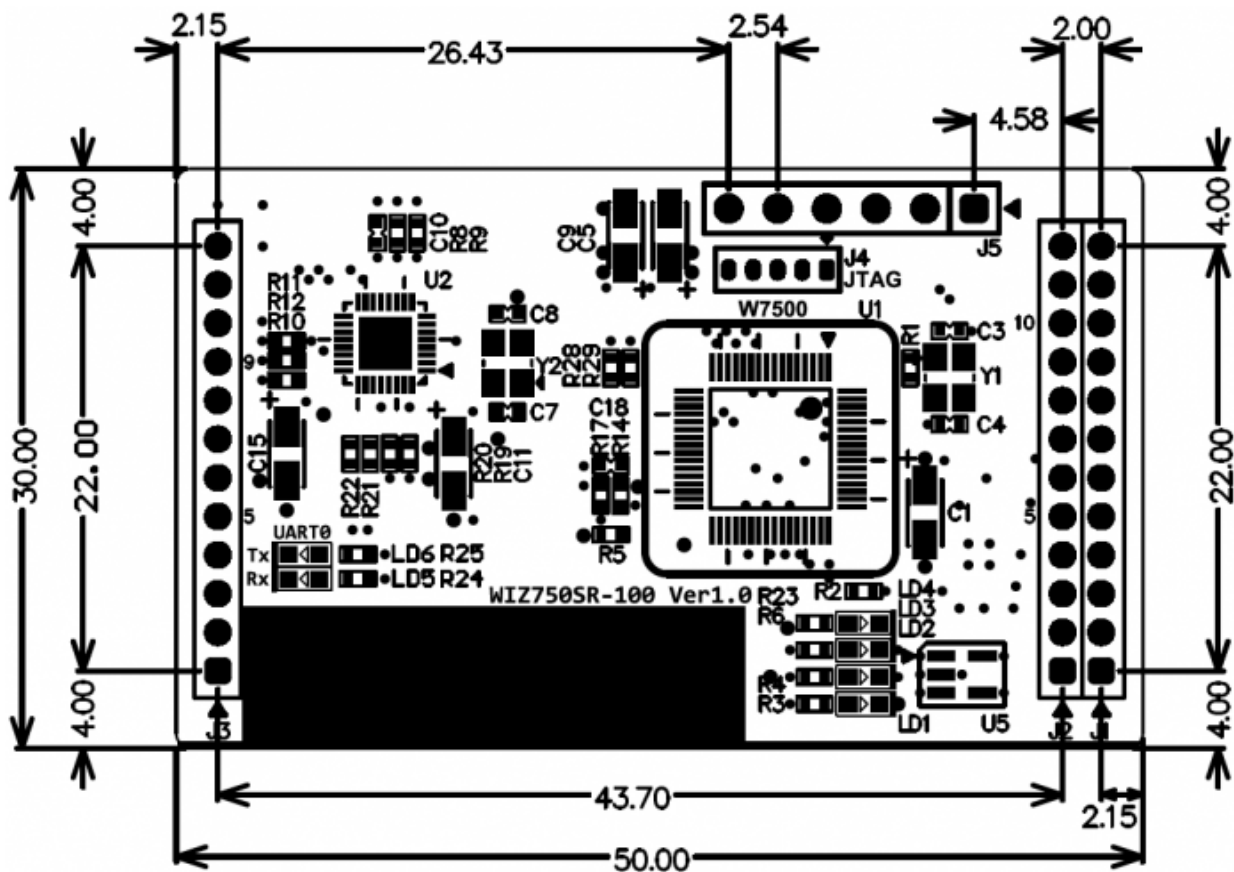
RJ-45 Connector (BS-RB10005)



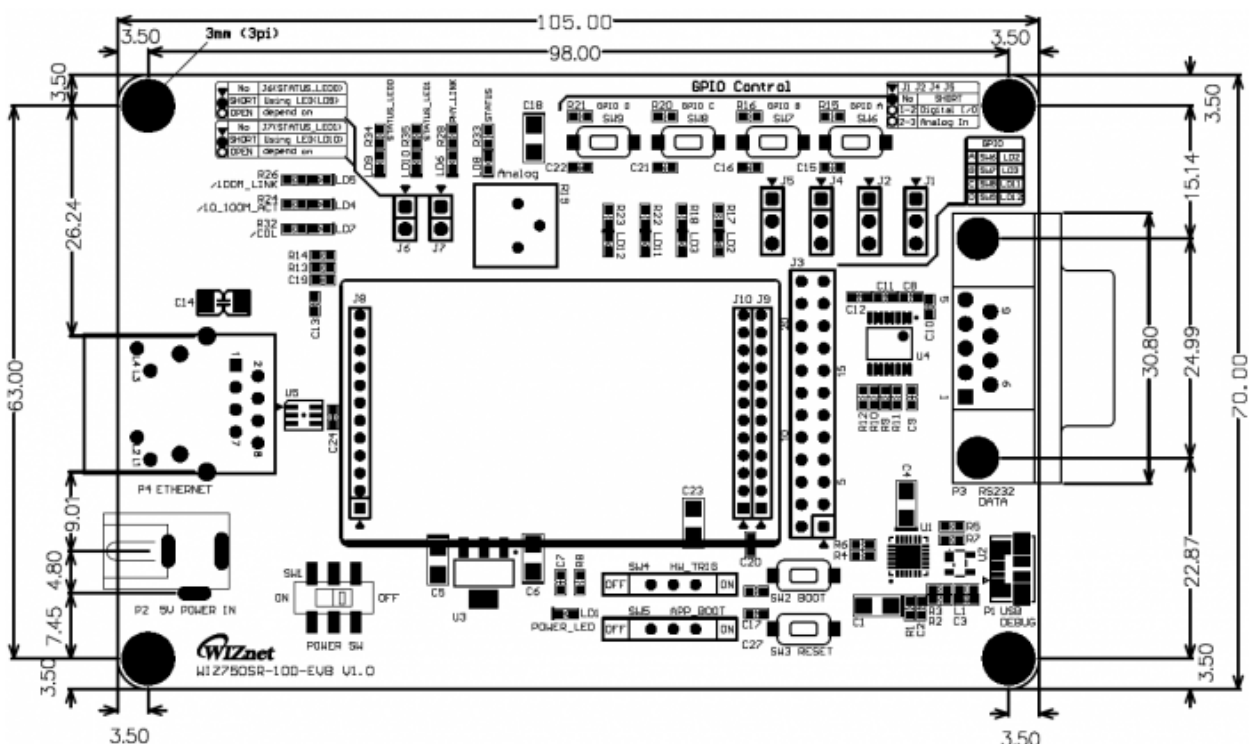
Pin Number	Pin	Signal
1	R1	TX+
2	R2	TX-
3	R3	TCT/RCT(Center tap)
4	R7	RX+
5	R8	RX-
6	L1+(Active LED)	Anode
7	L2- (Active LED)	Cathode
8	L3+(LINK LED)	Anode
9	L4- (LINK LED)	Cathode

Dimension

- WIZ750SR-100 Rev1.0 Dimension :
 - 50mm x 30mm (PCB board size)
 - 50mm x 30mm x 12mm (Included part size)



- WIZ750SR-100-EVB Rev1.0 Dimension :
 - 105mm x 70mm (PCB board size)
 - 105mm x 70mm x 18mm (Included part size)



Navigation



 Scroll to Top



 Back to Product Main

WIZ750SR series Common Documents

- [User's Manual](#)
- [Device Command Manual](#)
- [Troubleshooting Guide](#)
- [Update History](#)

WIZ750SR series Downloads

- [Software Downloads](#)
- [Technical Reference](#)

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- [WIZ750SR Getting Started Guide](#)
- [WIZ750SR Datasheet](#)

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- [WIZ750SR-100 Getting Started Guide](#)
- [WIZ750SR-100 Datasheet](#)

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- [WIZ750SR-105 Getting Started Guide](#)
- [WIZ750SR-105 Datasheet](#)

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- [WIZ750SR-110 Getting Started Guide](#)
- [WIZ750SR-110 Datasheet](#)