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RoHS

PART NO. : GCB0802C V1.0
-SFYLYHTC06

FOR MESSRS. : _____

CONTENTS

ACCEPTED BY: ---------- PROPOSED BY: ----------

RECORD OF REVISION

DATE	PAGE	SUMMARY

3. General specifications

3.1 General specifications

PLEASE REFER TO:

"CUSTOMER ACCEPTANCE STANDARD SPECIFICATIONS (MS-10-10000)".

3.2 Quality Assurance and Warranty

PLEASE REFER TO:

"QUALITY ASSURANCE MANUL (MS-10-10001)".

3.3 This individual specification is prior to general specifications

4. Mechanical data

- Display format: 8 characters x 2 lines
- Microprocessor interface: 8 bits Par all
- LCD type: STN positive Yellow-Green
- Backlight color: Yellow-Green
- Viewing angle: 6 o'clock
- LCD controller: S6A0069 OR equivalent
- Module size: 58x 32 x 12.6 mm
- View area: 37.8x16 mm
- Dot size: 0.56x 0.66 mm
- Dot pitch: 0.7 x 0.6mm
- Driving method: 1/16 duty, 1/5 bias

5. Absolute maximum ratings

5.1 Electrical absolute maximum ratings

I T E M	SYMBOL	MIN.	MAX.	UNIT	COMMENT
POWER SUPPLY FOR LOGIC	V_{DD}-V_{SS}	-0.3	6	V	---------
INPUT VOLTAGE	V_I	V_{SS}	V_{DD}	V	---------
STATIC ELECTRICITY	---------	------	-----	V	
POWER SUPPLY FOR BACKLIGHT	V_S	0	4.3	$V_{im's}$	---------
	f_{FL}	------	------	KHz	---------
STARTING VOLTAGE FOR BACKLIGHT	------	------	------	V_{rms}	Ta = 25℃
	------	------	------	V_{rms}	Ta = 25℃
POWER SUPPLY FOR LCD	V_{DD}-V_0	------	6	V	---------

5.2 Environmental absolute maximum ratings

I T E M	OPERATING		STORAGE		COMMENT
	MIN.	MAX.	MIN.	MAX.	
AMBIENT TEMPERATURE	-20℃	70℃	-30℃	80℃	------
HUMIDITY	NOTE (2)		NOTE (2)		NO CONDENSATION
VIBRATION NOTE (3)	------	0.5G	------	2G	10~300Hz XYZ DIRECTIONS 1 Hr EACH
SHOCK NOTE (3)	------	3G	------	5G	10 msec XYZ DIRECTIONS 1 TIME EACH
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACCEPTABLE		------

NOTE (2): Ta ≦ 70℃: 75% RH MAX.

Ta ＞ 70℃: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 75% RH AT 70℃.

NOTE (3): 1G = 9.8 m/s^2

6. Electrical characteristics

Ta = 25℃ VDD = 5.0 V

I T E M	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power supply voltage for circuit	V_{DD}-V_{SS}	------	4.75	5.0	5.25	V
Power supply voltage for LCD drive	V_{DD}-V_0	------	-----	4.7	------	V
LCD display duty ratio	DUTY	------	------	1/16	------	------
LED BACKLIGHT	Ifp	I mseo plus 10% Dutg cyele				mA
		Operating voltage	------	4.1	4.2	V
		Forward current		60	70	mA
LED Lifetime	------	V_{FL}= 4.1Vrms f_{FL}= KHz	------	100,000	------	Hr
Power supply LCD current	I_{EE}	V_{DD}-V_0= 4.7 V	------	----	------	mA

LED backlight: Due to the LED backlight working current is XXX Max,and LED chips Vop may be different, Gemini will adjust the backlight resistor according to the LED chips Vop, to meet the brightness maximum.

7. Optical characteristics

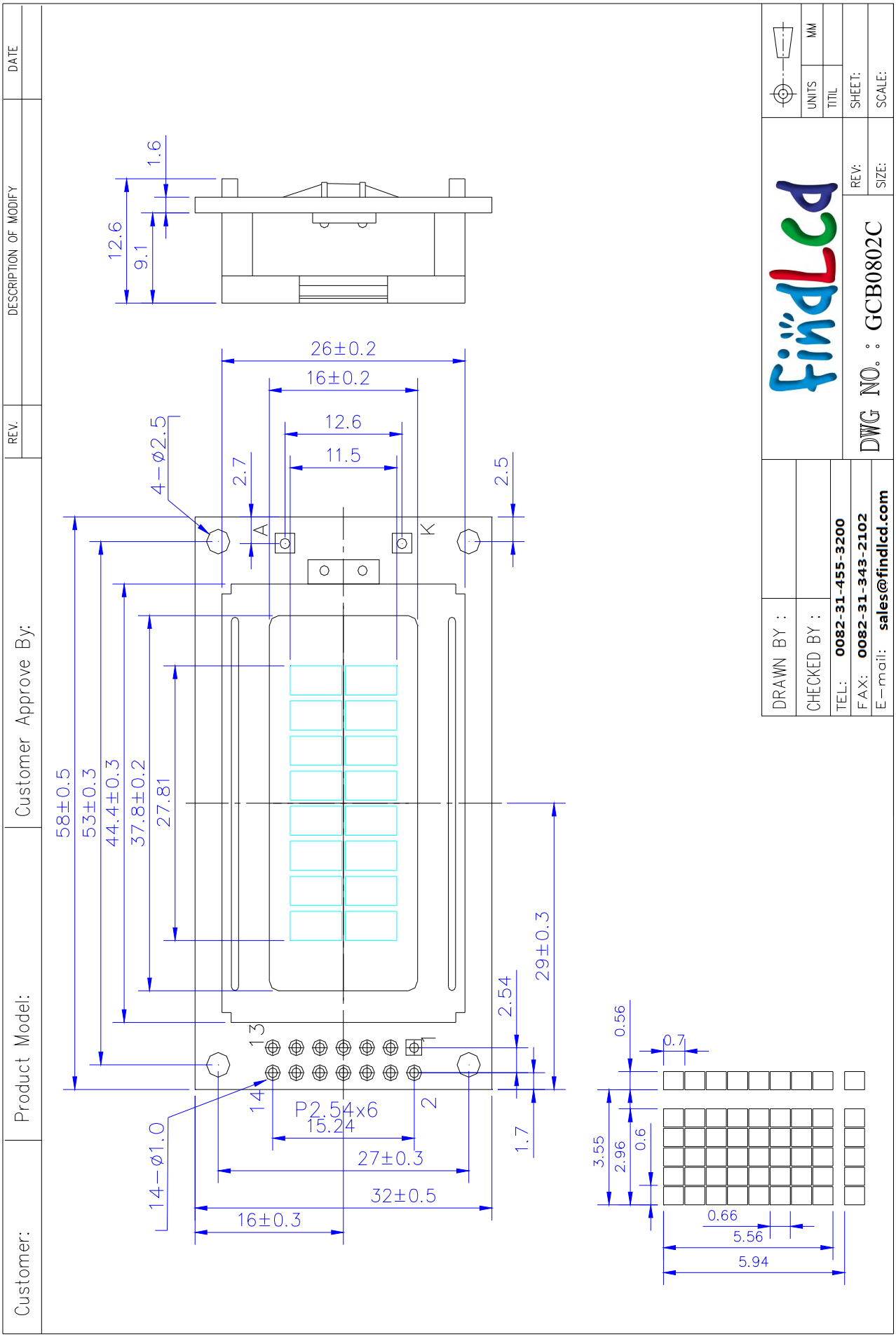
Ta = 25℃ V_{DD}-V_0 = 4.7V

I T E M	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Viewing angle	Φ2-Φ1	K ≧ 2.0	-35	----	20	deg.	1
Contrast ratio	K	Φ = 10° θ = 0°	4.0	----	-------	-------	1
Response time (at 25℃)	tr (rise)	Φ = 10° θ = 0°	-------	------	250	ms	1
	tf (fall)	Φ = 10° θ = 0°	-------	------	250	ms	1
The brightness of backlighting source	B	VFL=4.1Vrms fFL= KHZ	-------	200	-------	cd/m^2	2

NOTE (1): SEE CUSTOMER ACCEPTANCE STANDARD SPECIFICATION FOR DEFINITION OF OPTICAL CHARACTERISTICS

NOTE (2): UNDER NORMAL TEMPERATURE AND HUMIDITY IN A DARK ROOM

8. Outline dimension

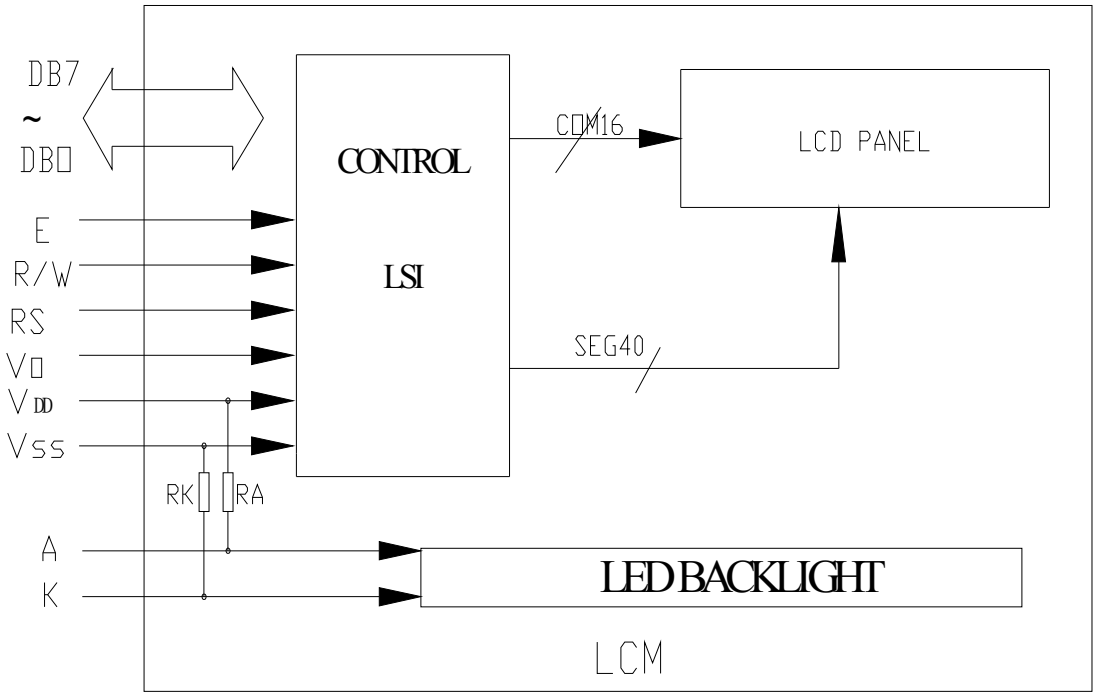


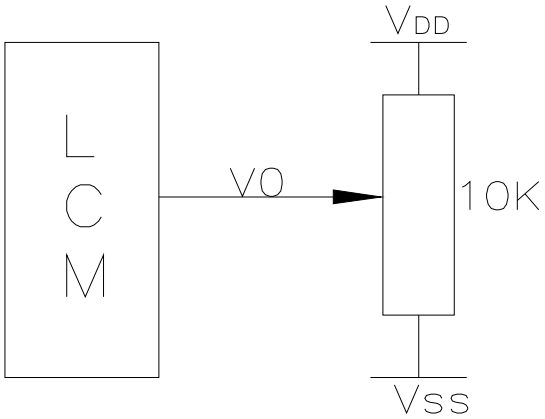
8.1 Interface

Pin Assignment

PIN NO.	Symbol	Leve	Function
1	Vss	0V	Ground
2	VDD	5.0V	Power supply for LCM (+)
3	V0	--	Contrast Adjust
4	RS	H/L	Register select signal
5	R/W	H/L	Data read / write
6	E	H/L	Enable signal
7	DB0	H/L	Data bus line
8	DB1	H/L	Data bus line
9	DB2	H/L	Data bus line
10	DB3	H/L	Data bus line
11	DB4	H/L	Data bus line
12	DB5	H/L	Data bus line
13	DB6	H/L	Data bus line
14	DB7	H/L	Data bus line
A	A	+4.1V/60mA	Power supply for LED (+)
K	K	0	Power supply for LED (+)

9. Block diagram



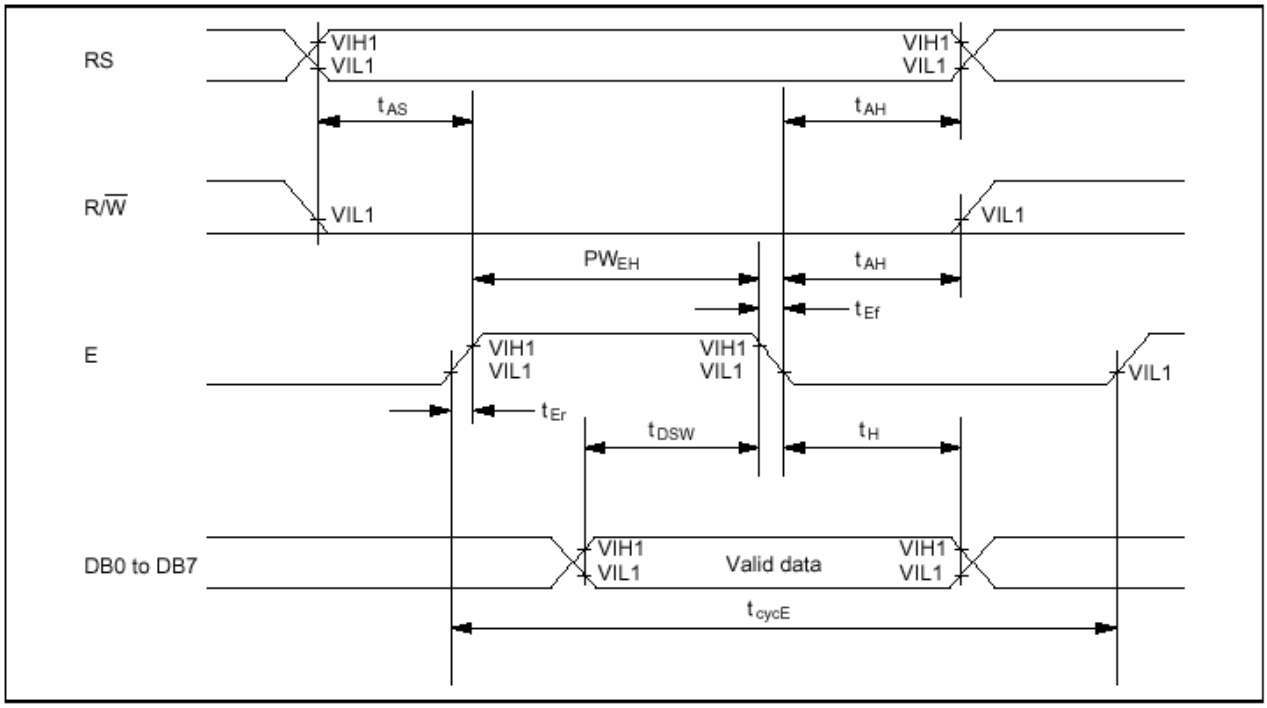


10.Interface Timing Chart

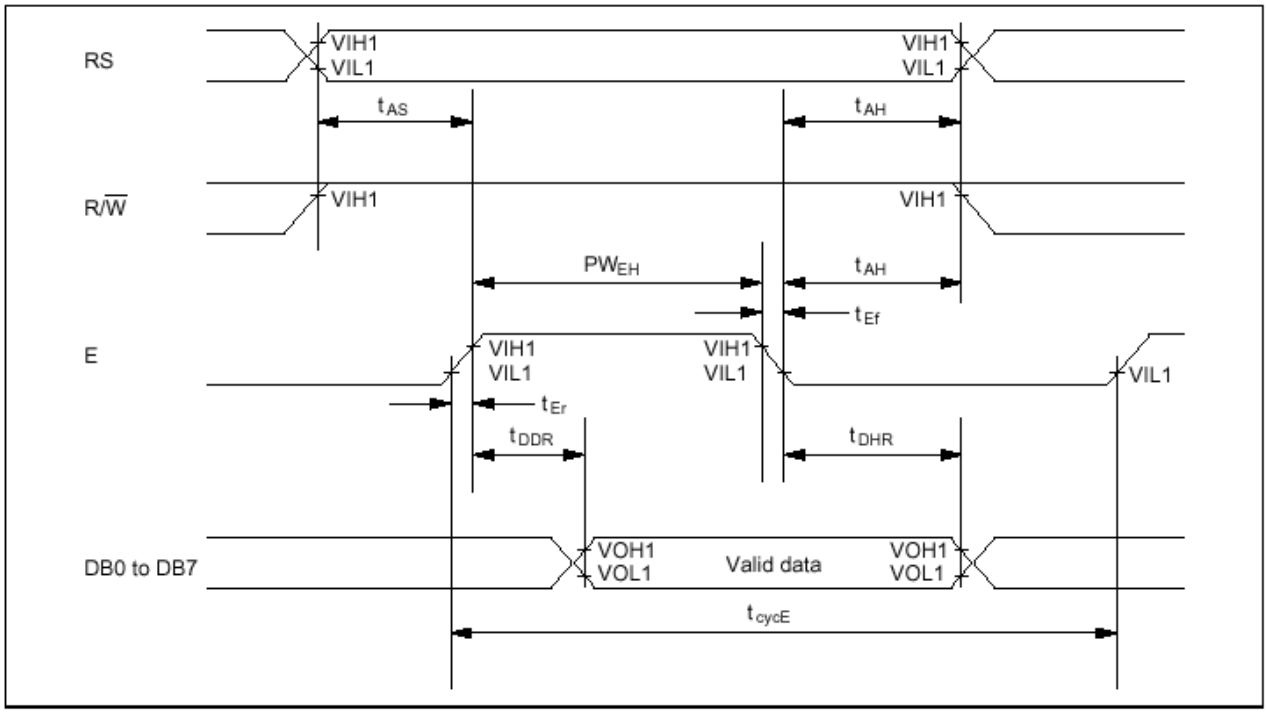
AC Characteristics(V_{DD}=4.5V~5.5V,Ta=-30~+85℃)

Mode	Characteristic	Symbol	Min.	Typ.	Max.	Unit
Write Mode (Refer to Fig-6)	E Cycle Time	tc	500	-	-	ns
	E Rise / Fall Time	t_R,t_F	-	-	20	
	E Pulse Width (High, Low)	tw	230	-	-	
	R/W and RS Setup Time	tsu1	40	-	-	
	R/W and RS Hold Time	t_{H1}	10	-	-	
	Data Setup Time	tsu2	80	-	-	
	Data Hold Time	t_{H2}	10	-	-	
Read Mode (Refer to Fig-7)	E Cycle Time	tc	500	-	-	ns
	E Rise / Fall Time	t_R,t_F	-	-	20	
	E Pulse Width (High, Low)	tw	230	-	-	
	R/W and RS Setup Time	tsu	40	-	-	
	R/W and RS Hold Time	t_H	10	-	-	
	Data Output Delay Time	t_D	-	-	120	
	Data Hold Time	t_{DH}	5	-	-	

Timing Characteristics



Write Operation



Read Operation

11.Instruction Code

Instruction Table

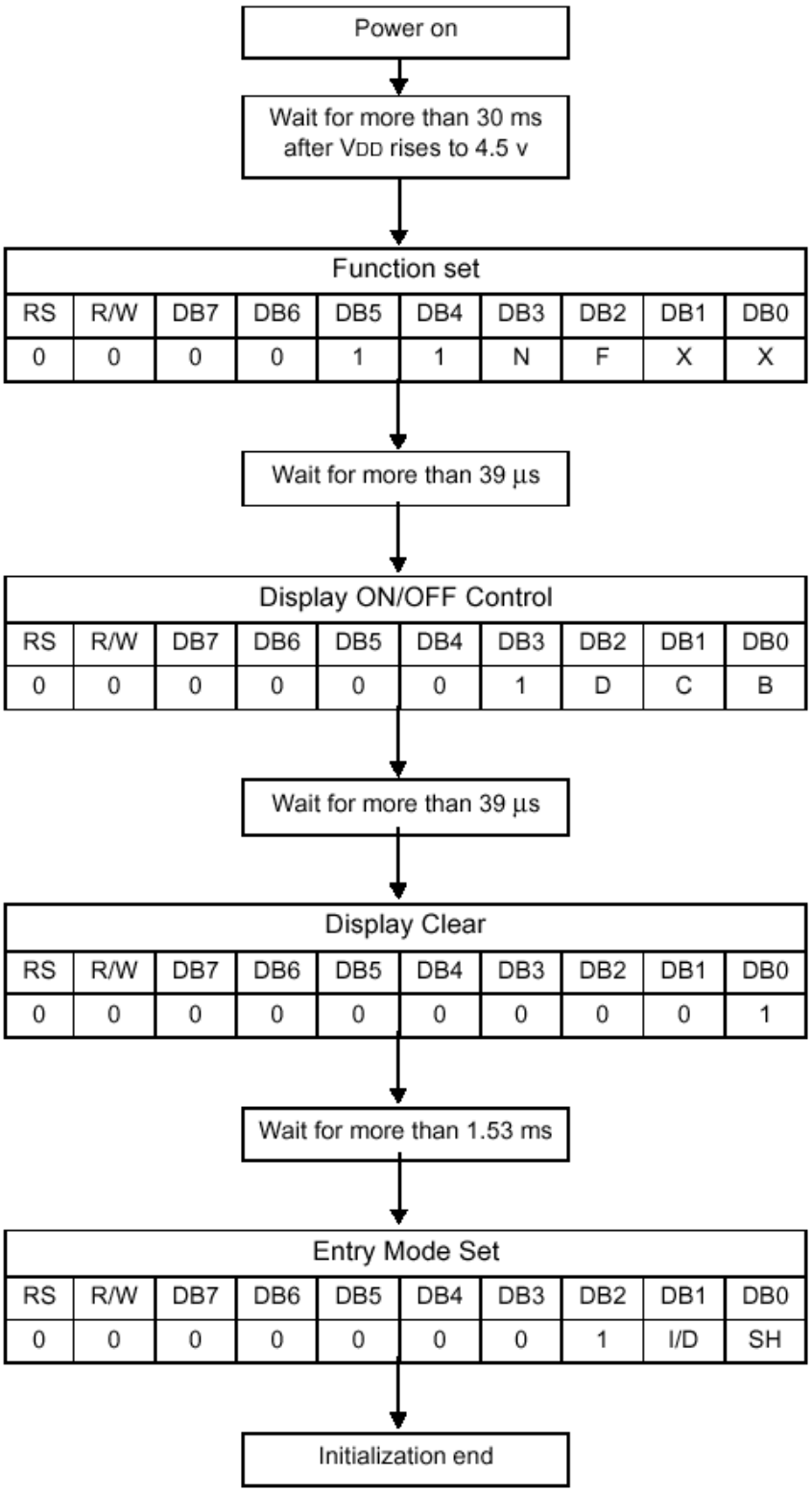
Instruction	Instruction Code										Description	Execution time (fosc= 270 kHz)
	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write “20H” to DDRAM and set DDRAM address to “00H” from AC	1.53 ms
Return Home	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to “00H” from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53 ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and enable the shift of entire display.	39 μs
Display ON/ OFF Control	0	0	0	0	0	0	1	D	C	B	Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.	39 μs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	39 μs
Function Set	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5×11dots/5×8 dots)	39 μs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39 μs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39 μs
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 μs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43 μs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43 μs

* “-“: don’t care

NOTE: When an MPU program with checking the Busy Flag(DB7) is made, it must be necessary 1/2Fosc is necessary for executing the next instruction by the falling edge of the 'E' signal after the Busy Flag (DB7) goes to “Low”.

DISPLAY CHARACTER ADDRESS CODE																		
Display position:	1	2	3	4	5	6	7	8										
Address line1:	00	01	02	03	04	05	06	07										
Address line2:	40	41	42	43	44	45	46	47										

8-bit interface mode (Condition: fosc = 270KHZ)



N	0	1-line mode
	1	2-line mode

F	0	display off
	1	display on

D	0	display off
	1	display on

C	0	cursor off
	1	cursor on

B	0	blink off
	1	blink on

I/D	0	decrement mode
	1	increment mode

SH	0	entire shift off
	1	entire shift on

12.Character generator ROM

13. Specification of quality assurance

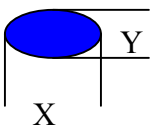
AQL inspection standard

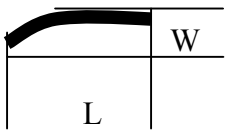
Sampling method: MIL-STD-105E, Level II, single sampling

Defect classification **(Note: * is not including)**

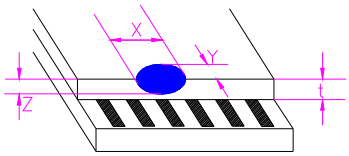
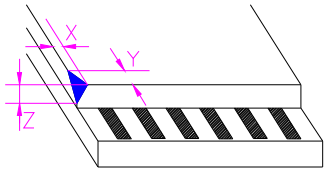
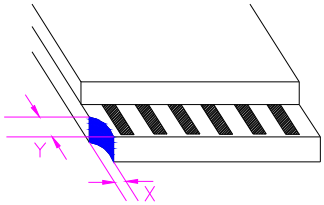
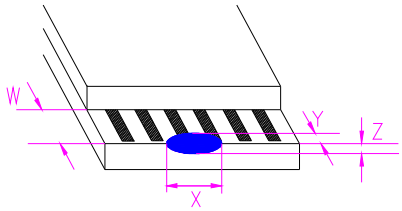
Classify	Item		Note	AQL
Major	Display state	Short or open circuit	1	0.65
		LC leakage		
		Flickering		
		No display		
		Wrong viewing direction		
		Contrast defect (dim, ghost)	**2**	
		Back-light	**1,8**	
	Non-display	Flat cable or pin reverse	**10**	
		Wrong or missing component	**11**	
Minor	Display state	Background color deviation	**2**	**1.0**
		Black spot and dust	**3**	
		Line defect, Scratch	**4**	
		Rainbow	**5**	
		Chip	**6**	
		Pin hole	**7**	
	Polarizer	Protruded	**12**	
		Bubble and foreign material	**3**	
	Soldering	Poor connection	**9**	
	Wire	Poor connection	**10**	
	TAB	Position, Bonding strength	**13**	

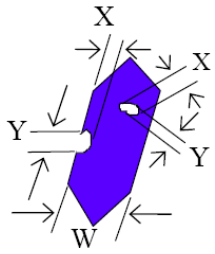
Note on defect classification

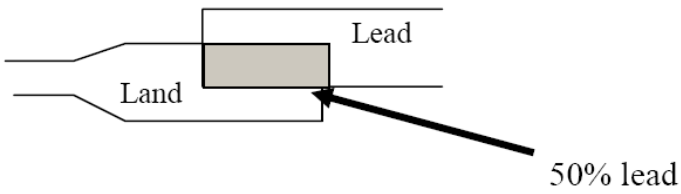
<table>
<tr><th>No.</th><th>Item</th><th>Criterion</th></tr>
<tr><td rowspan="6">1</td><td>Short or open circuit</td><td rowspan="6">Not allow</td></tr>
<tr><td>LC leakage</td></tr>
<tr><td>Flickering</td></tr>
<tr><td>No display</td></tr>
<tr><td>Wrong viewing direction</td></tr>
<tr><td>Wrong Back-light</td></tr>
<tr><td>2</td><td>Contrast defect
Background color deviation</td><td>Refer to approval sample</td></tr>
<tr><td>3</td><td>Point defect, Black spot, dust (including Polarizer)
ϕ = (X+Y)/2</td><td> 

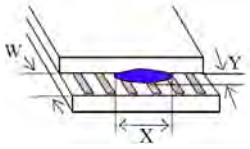
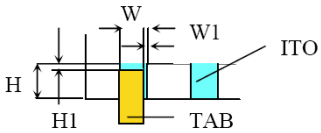
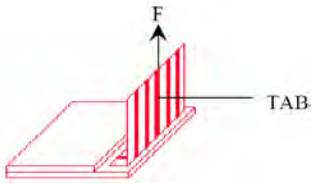
<table>
<tr><th>Point Size</th><th>Acceptable Qty.</th></tr>
<tr><td>ϕ<0.10</td><td>Disregard</td></tr>
<tr><td>0.10<ϕ≤0.20</td><td>3</td></tr>
<tr><td>0.20<ϕ≤0.25</td><td>2</td></tr>
<tr><td>0.25<ϕ≤0.30</td><td>1</td></tr>
<tr><td>ϕ>0.30</td><td>0</td></tr>
</table>
Unit:mm</td></tr>
<tr><td>4</td><td>Line defect, Scratch</td><td> 

<table>
<tr><th colspan="2">Line</th><th>Acceptable Qty.</th></tr>
<tr><td>L</td><td>W</td><td></td></tr>
<tr><td>---</td><td>0.015≥W</td><td>Disregard</td></tr>
<tr><td>3.0≥L</td><td>0.03≥W</td><td rowspan="2">2</td></tr>
<tr><td>2.0≥L</td><td>.05≥W</td></tr>
<tr><td>1.0≥L</td><td>0.1>W</td><td>1</td></tr>
<tr><td>---</td><td>0.05<W</td><td>Applied as point defect</td></tr>
</table></td></tr>
<tr><td>5</td><td>Rainbow</td><td>Not more than two color changes across the viewing area.</td></tr>
</table>

NO.	Item	Criterion
6	Chip Remark: X: Length direction Y: Short direction Z: Thickness direction t: Glass thickness W: Terminal Width	 Acceptable criterion X: ⩽2 / Y: 0.5mm / Z: ⩽t/2  Acceptable criterion X: ⩽2 / Y: 0.5mm / Z: ⩽t  Acceptable criterion X: ⩽3 / Y: ⩽2 / Z: ⩽t shall not reach to ITO  Acceptable criterion X: Disregard / Y: ⩽0.2 / Z: ⩽t

<table>
<tr><th>No.</th><th>Item</th><th>Criterion</th></tr>
<tr><td>7</td><td>Segment
pattern
W = Segment width
ϕ = (X+Y)/2</td><td>(1) Pin hole
ϕ < 0.10mm is acceptable.
 

<table>
<tr><th>Point Size</th><th>Acceptable Qty</th></tr>
<tr><td>ϕ≤1/4W</td><td>Disregard</td></tr>
<tr><td>1/4W< ϕ≤1/2W</td><td>1</td></tr>
<tr><td>ϕ>1/2W</td><td>0</td></tr>
</table>
Unit: mm</td></tr>
<tr><td>8</td><td>Back-light</td><td>(1) The color of backlight should correspond its specification.
(2) Not allow flickering</td></tr>
<tr><td>9</td><td>Soldering</td><td>(1) Not allow heavy dirty and solder ball on PCB.
(The size of dirty refer to point and dust defect)
(2) Over 50% of lead should be soldered on Land.
 
</td></tr>
<tr><td>10</td><td>Wire</td><td>(1) Copper wire should not be rusted
(2) Not allow crack on copper wire connection.
(3) Not allow reversing the position of the flat cable.
(4) Not allow exposed copper wire inside the flat cable.</td></tr>
<tr><td>11*</td><td>PCB</td><td>(1) Not allow screw rust or damage.
(2) Not allow missing or wrong putting of component.</td></tr>
</table>

NO.	Item	Criterion
12	Protruded W: Terminal Width	 Acceptable criteria: Y ≤ 0.4
13	TAB	1. Position  W1≤1/3W H1≤1/3H 2 TAB bonding strength test  P (=F/TAB bonding width) ≥650gf/cm , (speed rate: 1mm/min) 5pcs per SOA (shipment)
14	Total no. of acceptable Defect	A. Zone Maximum 2 minor non-conformities per one unit. Defect distance: each point to be separated over 10mm B. Zone It is acceptable when it is no trouble for quality and assembly in customer's end product.